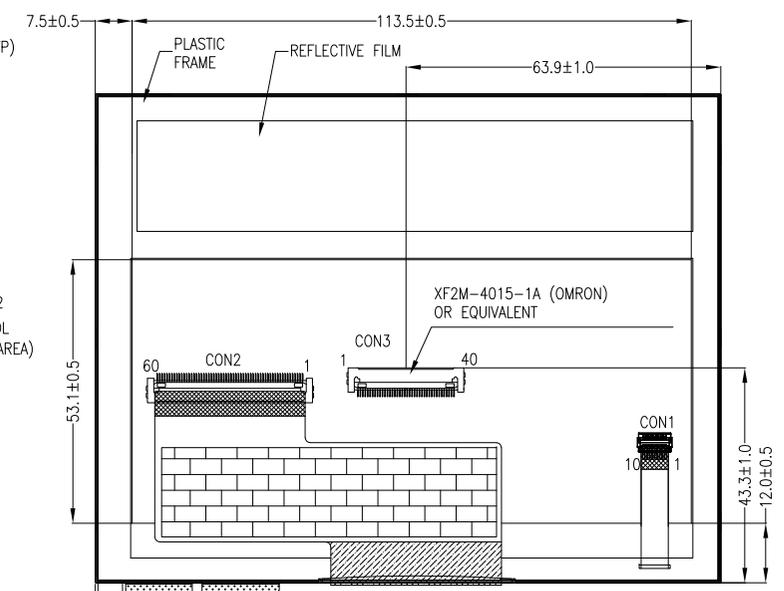
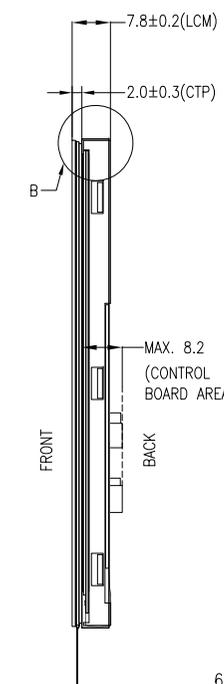
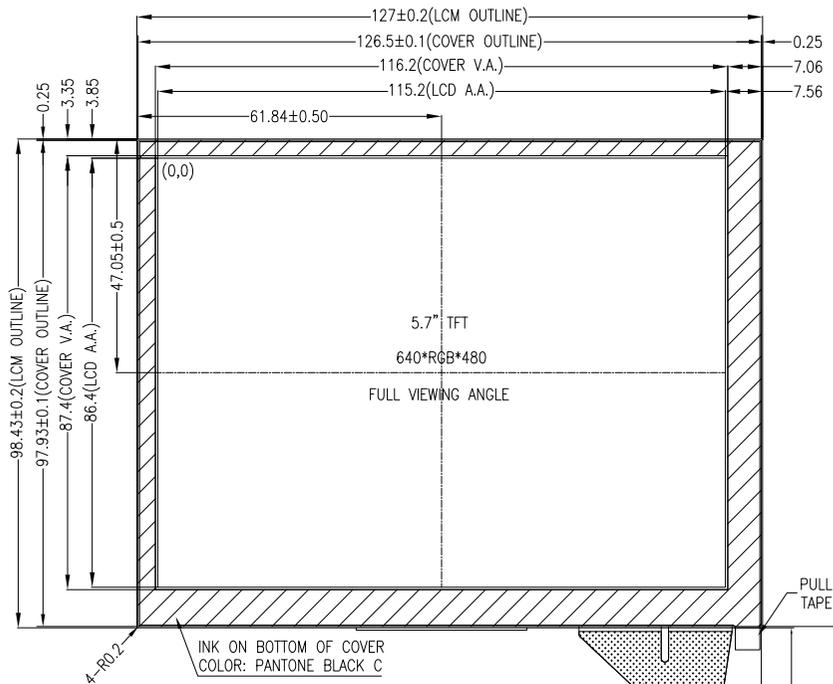


FOR ANY TOUCH PANEL OR OTHER ACCESSORIES ASSEMBLED WITH FRAME GLUE, A PERMANENT AND RELIABLE MECHANICAL STRUCTURE NEEDS TO BE DESIGNED ON CUSTOMER'S WHOLE MACHINE TO HOLD THEM IN PLACE TO AVOID LOOSE.



NOTES FOR TFT:

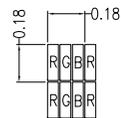
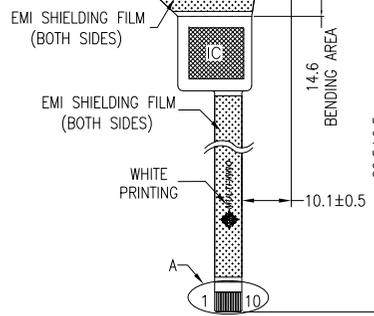
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: JD9168S
5. BACKLIGHT TYPE: WHITE LED  
BUILT-IN BLACKLIGHT DRIVER: VLED=5.0V(TYP.)
6. TOP POLARIZER SURFACE TREATMENT: GLARE

NOTES FOR CTP:

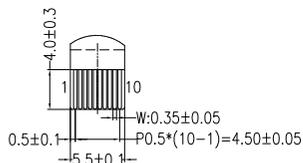
1. 5.7 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VDD=3.3V
4. DRIVER IC: FT7311DQQ(SMT)
5. INTERFACE: IIC
6. RESOLUTION: 640\*480
7. MULTI-FINGER: UP TO 5
8. HARDNESS OF COVER SURFACE: 7H(750g)
9. FIRMWARE FILE:  
MI0570CBP-C6A\_FIRMWARE\_V01\_20250325\_ALL.BIN

NOTES FOR OTHERS:

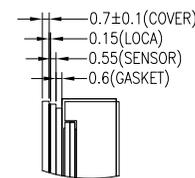
1. OPERATING TEMP: -30°C ~ 80°C
2. STORAGE TEMP: -30°C ~ 80°C
3. SURFACE LUMINANCE: 600 CD/M<sup>2</sup> (TYP.)
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. UNMARKED TOLERANCE: ±0.3MM
6. ROHS COMPLIANT



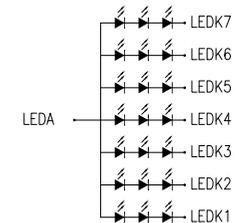
PIXEL DETAIL (SCALE 40:1)



DETAIL A (SCALE 2:1)



DETAIL B (SCALE 2:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT FOR CON3

PIN	SYMBOL	PIN	SYMBOL
1	U/D	21	G5
2	NC	22	G4
3	HSYNC	23	G3
4	VLED	24	VSS
5	VLED	25	G2
6	VLED	26	G1
7	VCC	27	G0
8	VSYNC	28	VSS
9	DE	29	R5
10	NC (XL)	30	R4
11	NC (YU)	31	R3
12	ADJ	32	VSS
13	B5	33	R2
14	B4	34	R1
15	B3	35	R0
16	VSS	36	NC (XR)
17	B2	37	NC (YD)
18	B1	38	DCLK
19	B0	39	VSS
20	VSS	40	L/R

PIN ASSIGNMENT FOR CTP

PIN	SYMBOL
1	GND
2	VDD
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	GND

**MIT** MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	TUFFY	SCALE:	1/1	MODULE P/N:	MI0570AET-2CP-A
CHECKED BY:		UNIT:	mm	DESCRIPTION:	TFT+CTP MODULE
APPROVED BY:		SHEET NO:	1/1		

VER.	REVISD DESCRIPTION	REVISER	DATE	VERSION NO:
01	FIRST ISSUE	TUFFY	2025.05.16	